



Diversified Applications Engineering Technologies

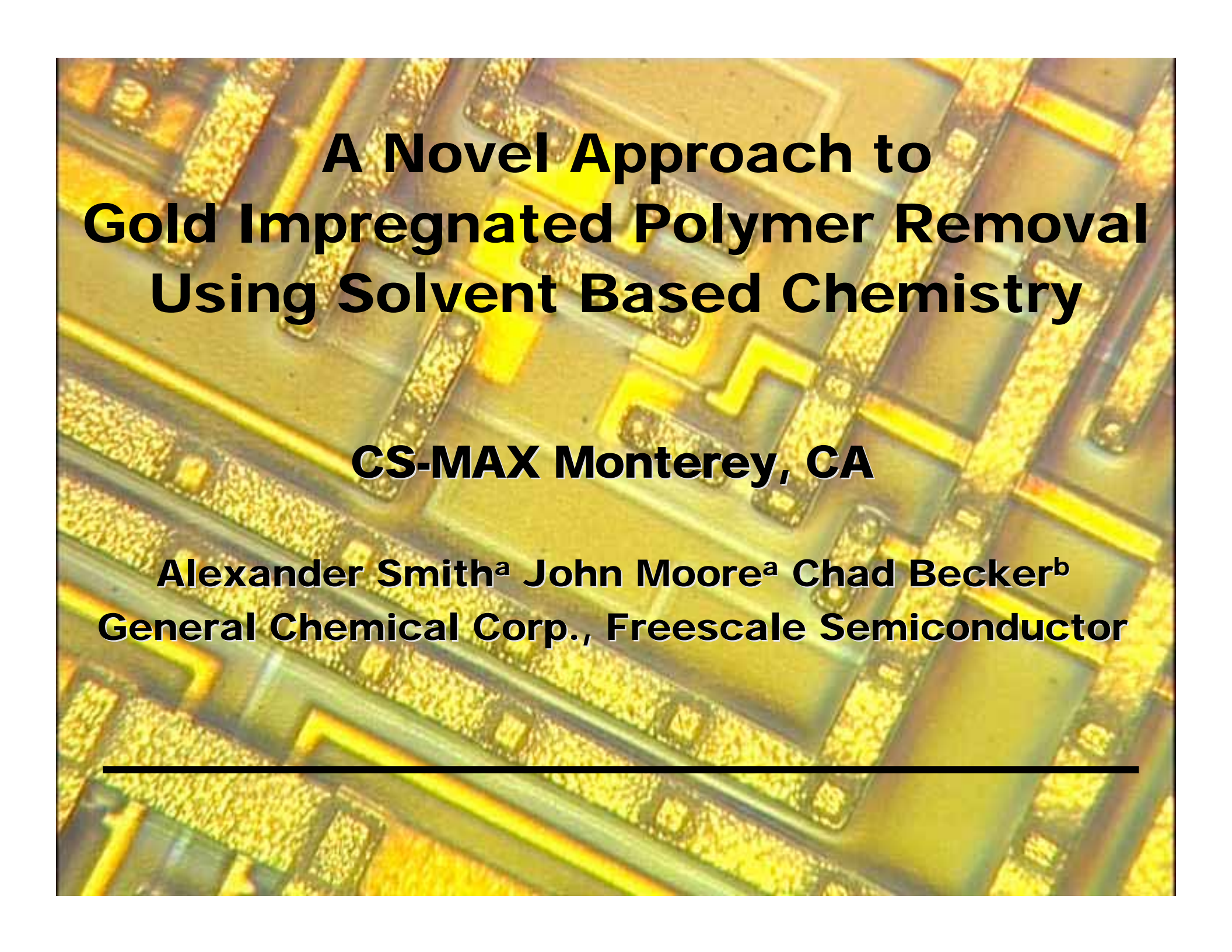
For further information concerning the following topic,
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www.daetec.com

**Contact DAETEC for more information
(805) 484-5546, www.daetec.com**

A microscopic view of a semiconductor chip showing a complex pattern of gold-impregnated polymer removal. The image displays a dense network of interconnected lines and structures, with a prominent yellowish-gold color. The background is a dark, textured surface, likely the underlying substrate or a protective layer. The overall appearance is that of a highly detailed, multi-layered microstructure.

A Novel Approach to Gold Impregnated Polymer Removal Using Solvent Based Chemistry

CS-MAX Monterey, CA

Alexander Smith^a John Moore^a Chad Becker^b
General Chemical Corp., Freescale Semiconductor

Outline

Etch Residue Formation

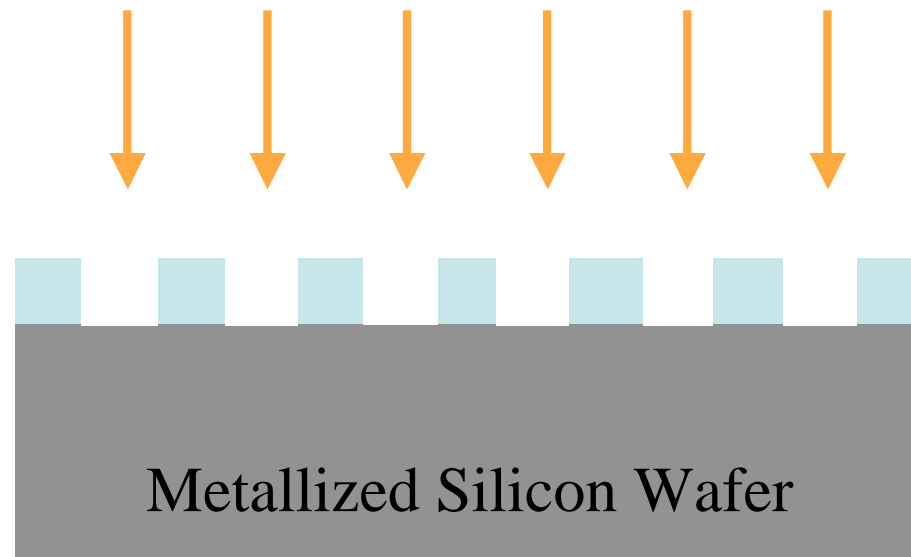
Characterization

Metal Coated Residue Removal

Summary

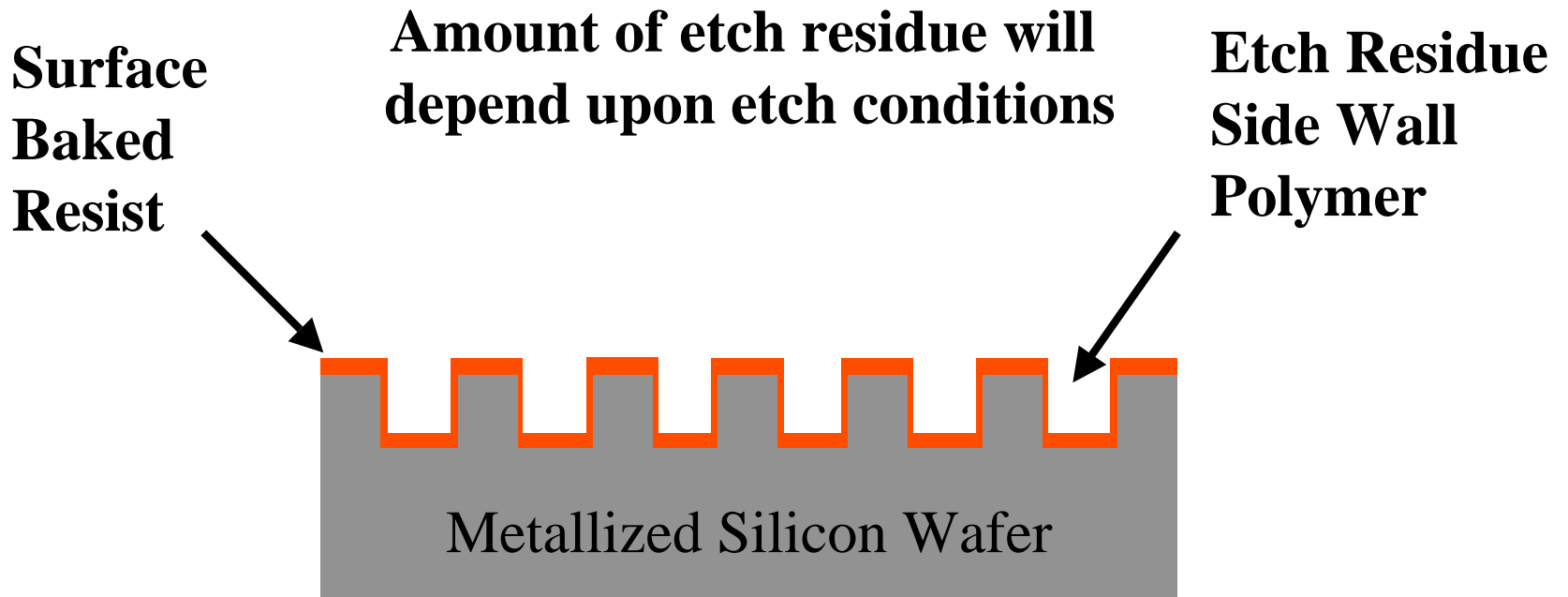
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Resist Patterning



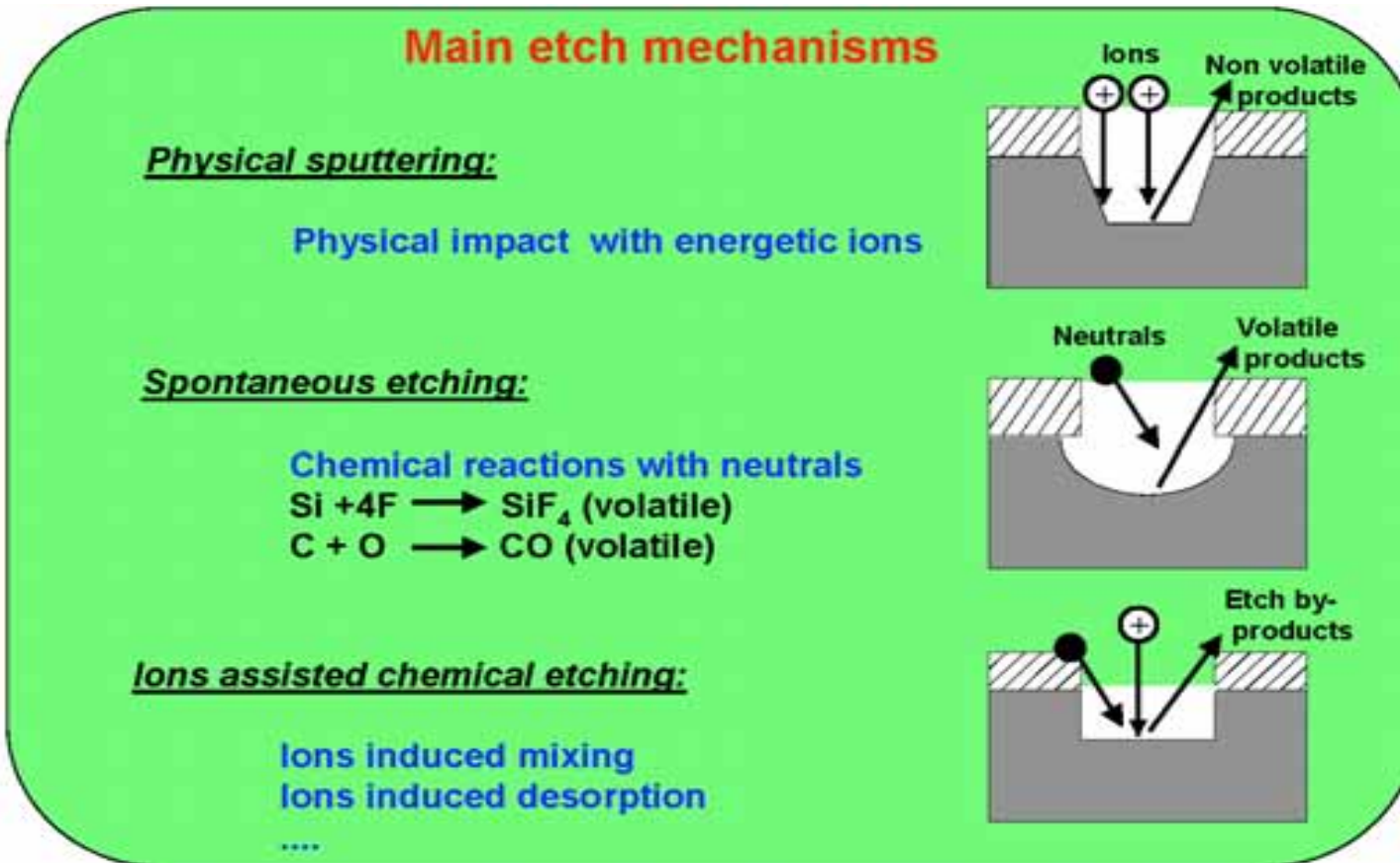
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Etch Residue Formation



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Plasma Etch Fundamentals

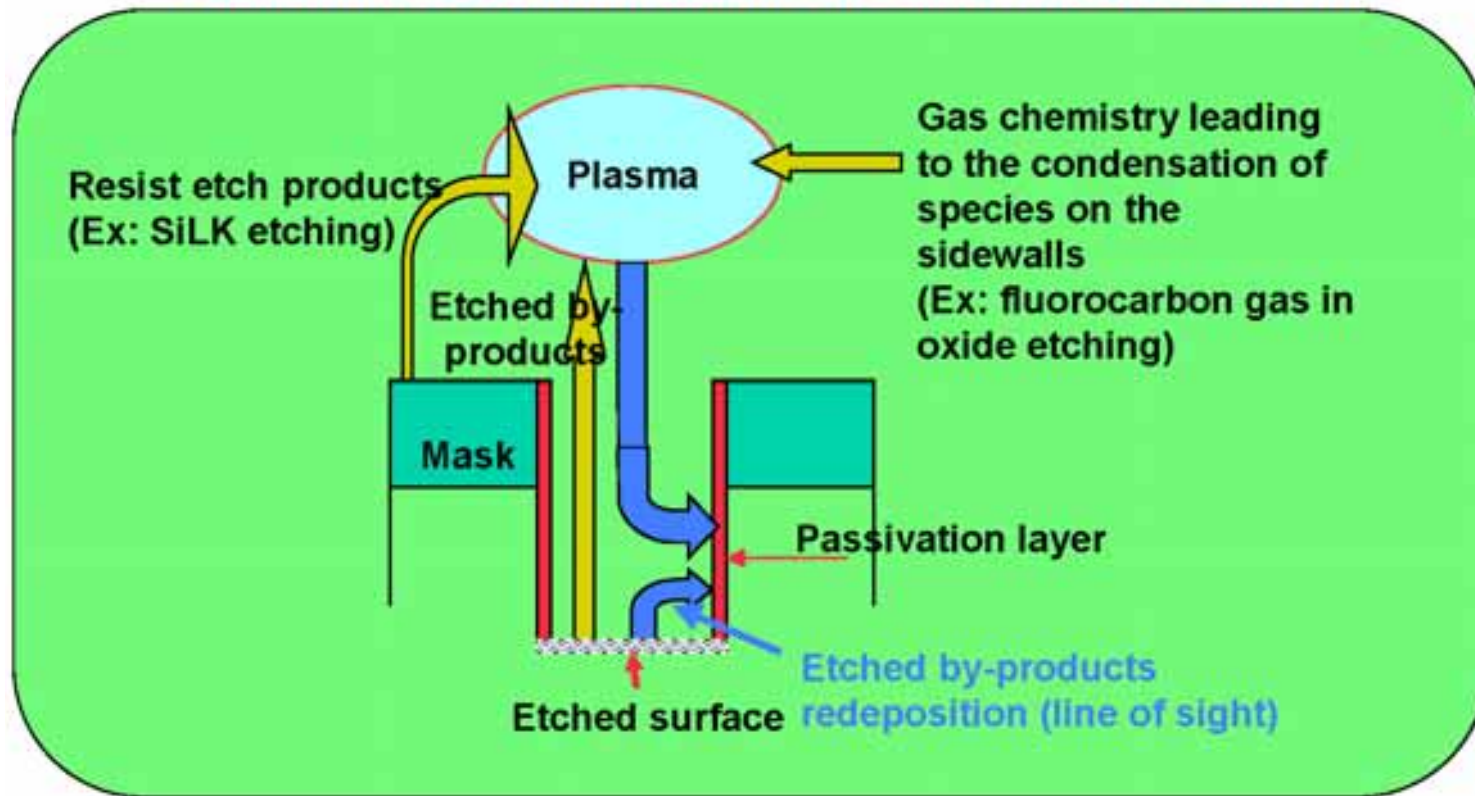


Courtesy: Chevolleau, T., LTM/CNRS, IITC 2003

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Plasma Etch Fundamentals

Passivation layer formation on the sidewalls of the patterns



Courtesy: Chevolleau, T., LTM/CNRS, IITC 2003

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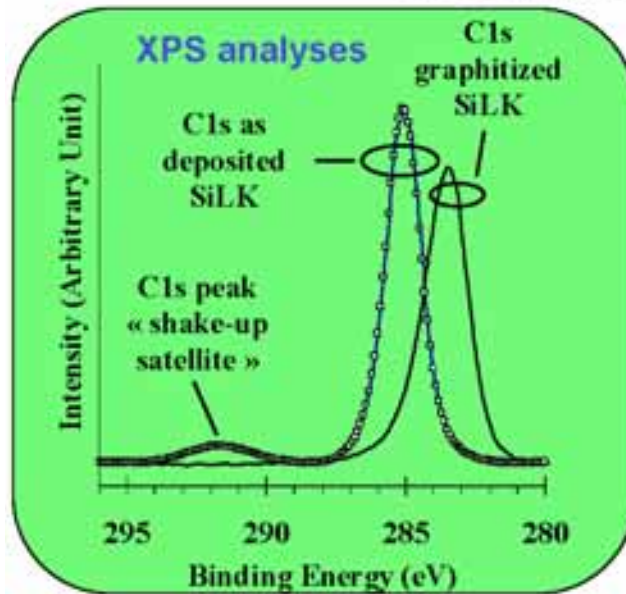
Etch Anisotropy on Dielectric

Graphitization phenomenon

Hydrogen components are sputtered away by ion bombardment



Carbon skeleton of the SiLK



Physical etching conditions
(Ar plasma)

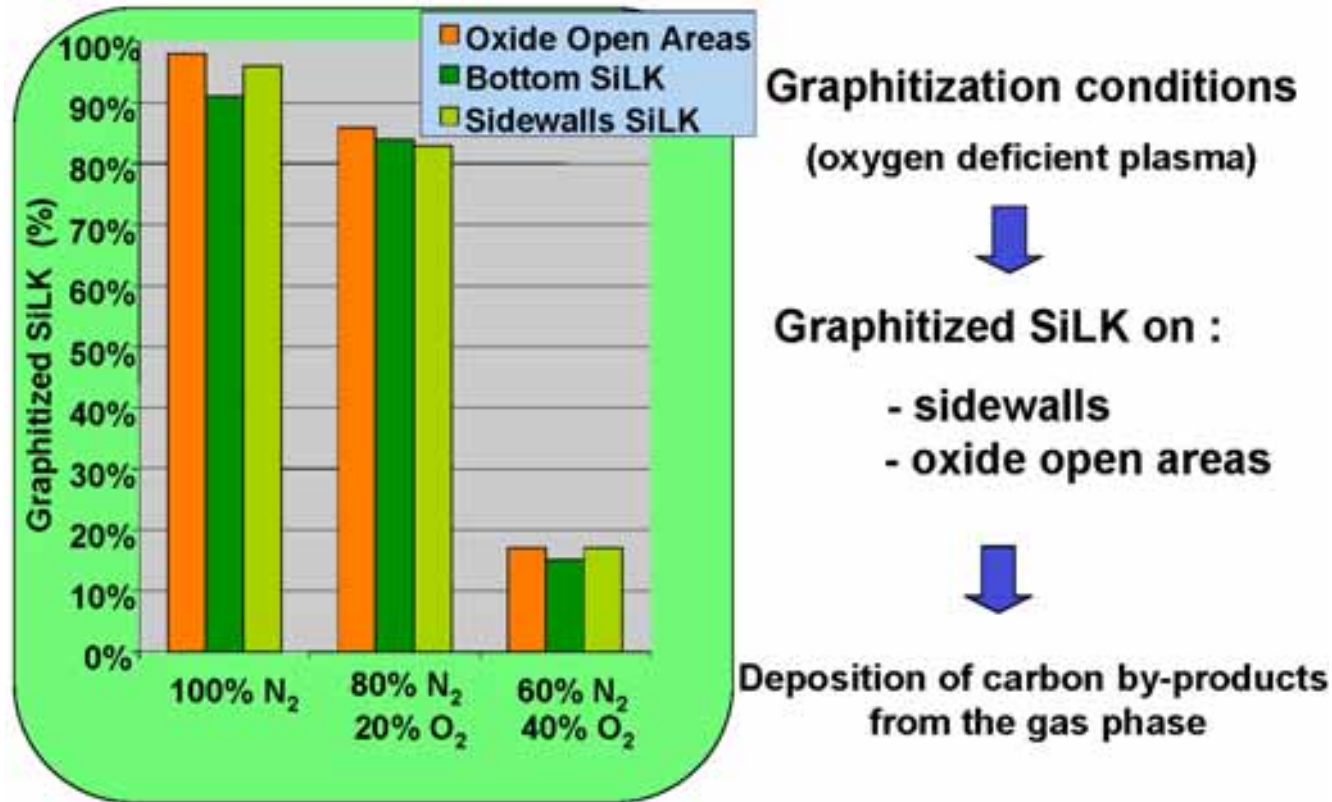


Shift of C1s SiLK™
from 285 eV (as deposited)
to 283,5 eV (graphite signature)

Courtesy: Chevolleau, T., LTM/CNRS, IITC 2003

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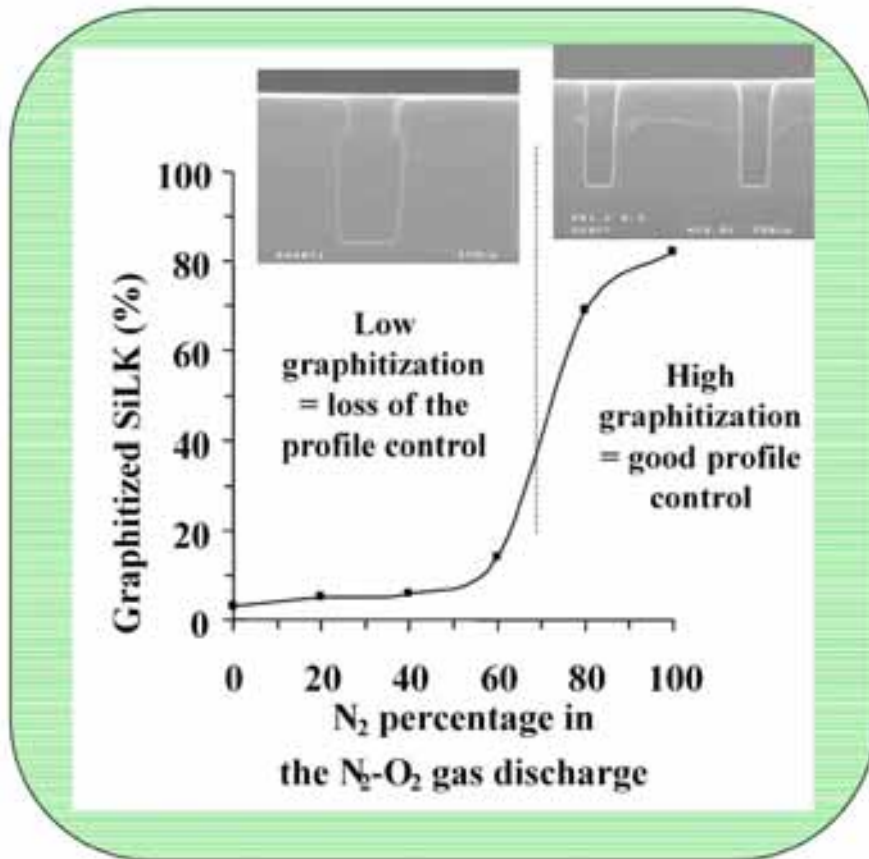
Etch Anisotropy on Dielectric



Courtesy: Chevolleau, T., LTM/CNRS, IITC 2003

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Etch Anisotropy on Dielectric



Sidewall Graphitization



**better profile control
(no lateral etching)**

Courtesy: Chevolleau, T., LTM/CNRS, IITC 2003

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Au Etch Residue Removal

Applications for GenSolve™ 525-D

III-V Compound Semiconductor

Frontside Via Etch through Organic
Layer (Polyimide or BCB)

Backside Via Etch through Substrate
to Gold Contact

Rework

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Au Etch Residue Removal

Conventional Process

- * Mineral Acid Immersion with KI
 - * DI Rinse + SRD
 - * Resist Strip in Specialty Chemistry
 - * DI Rinse + SRD
- - - Depending upon nature of resist, additional immersion steps may be required...

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Etch Rate

GenSolve™ 525-D

Au Removal:

20-60 Å/min

(depends upon GenSolve 525-D choice)

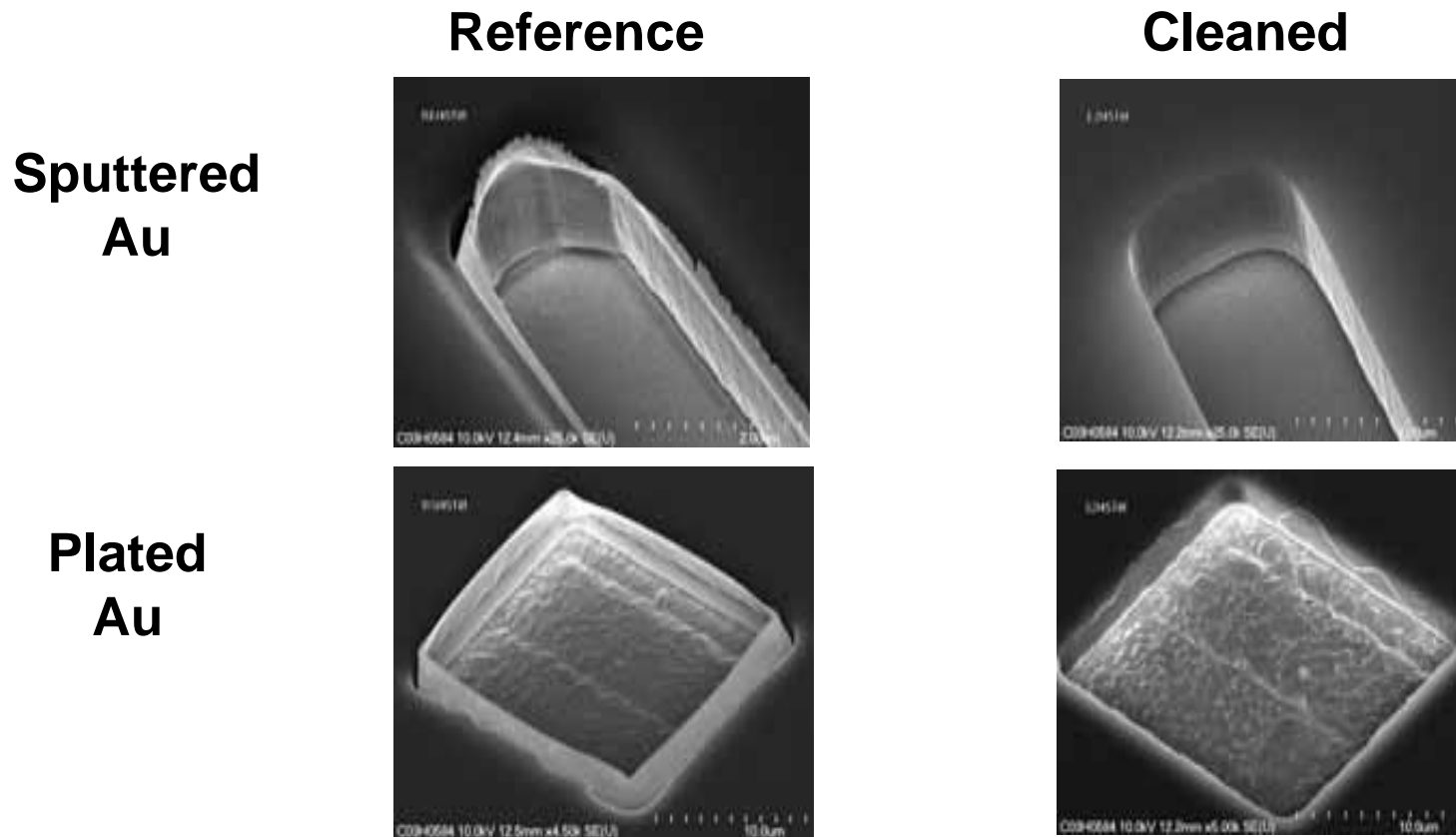
100Å removed

(60-70C, 1-2min)

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Au Etch Residue Removal

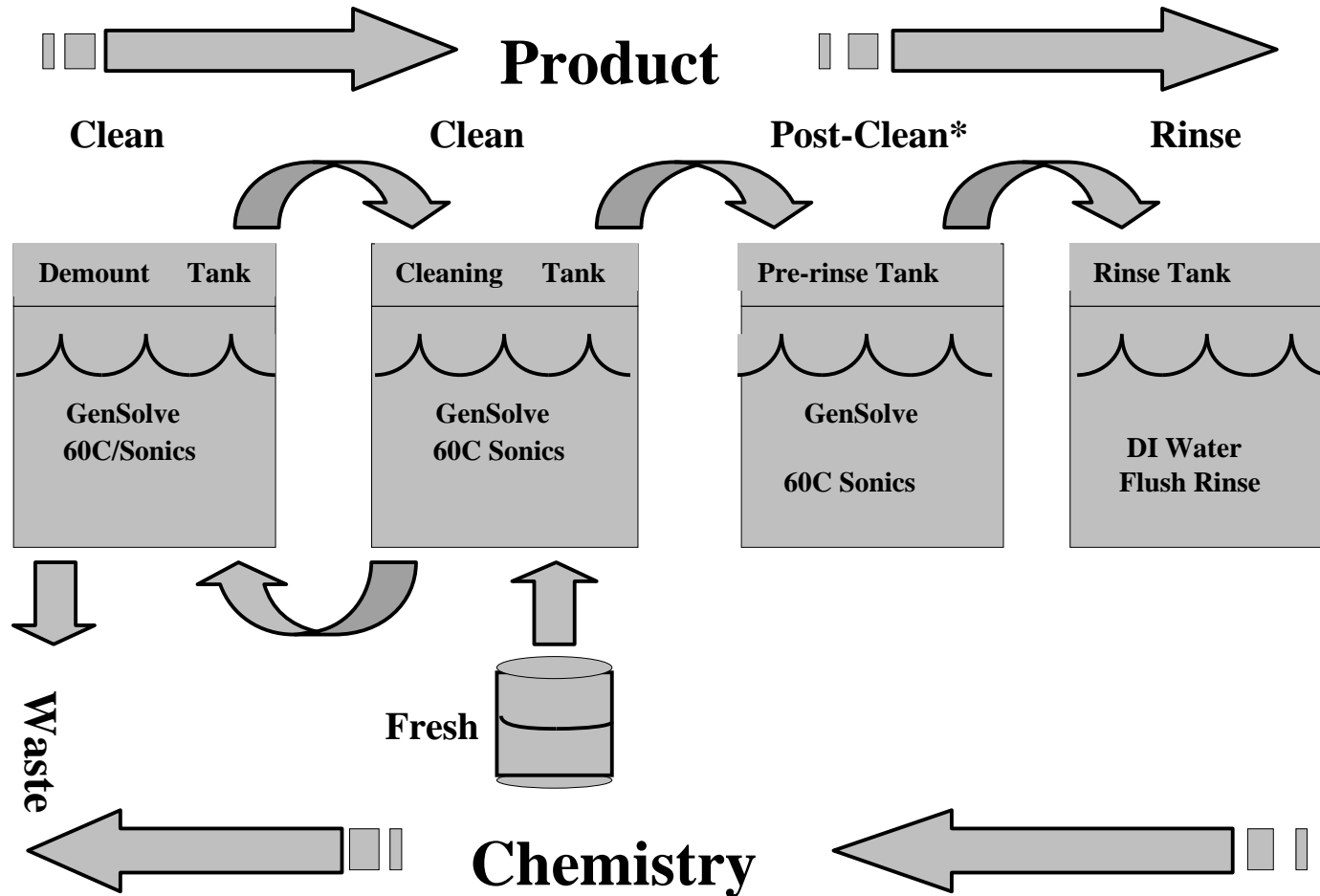
GenSolve™ 525-D (Batch Wafer Immersion Clean)



Process Recommendation: GenSolve™ 525-D, 60-70C, 2 minutes, DI Rinse

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Clean and Recirc Process



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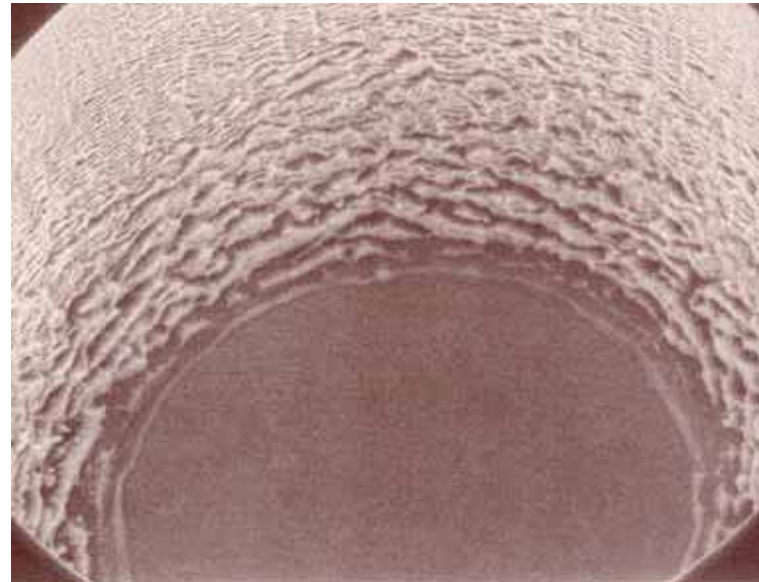
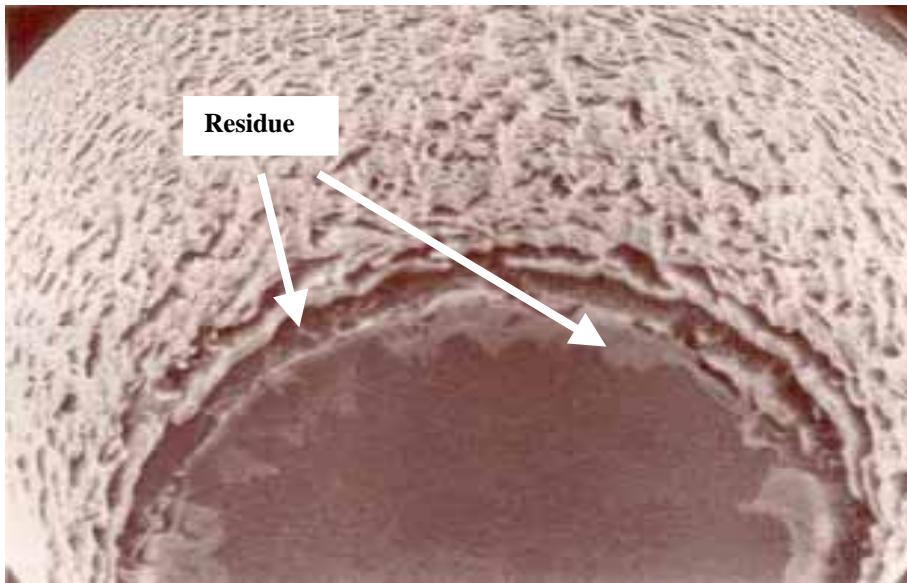
Au Etch Residue Removal

GenSolve™ 525-D (Single Wafer Tool Clean)

Backside Via Cleaning - 90s, single wafer tool

Reference

Cleaned

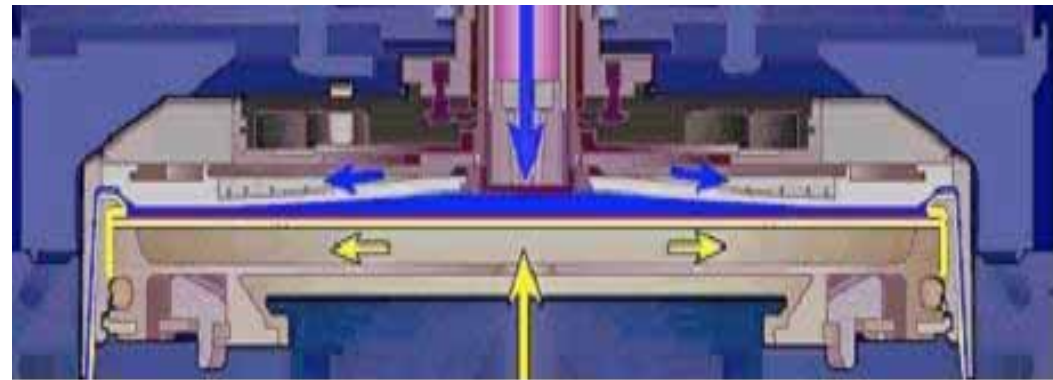
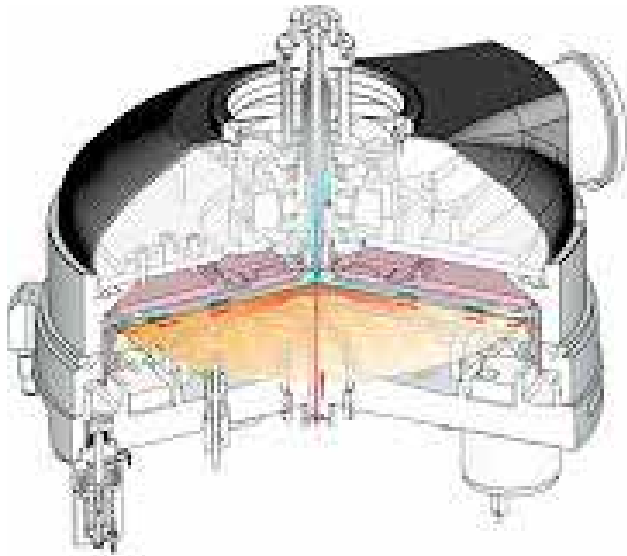


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Au Etch Residue Removal

GenSolve™ 525-D

Semitoool Capsule™ Single-Wafer Tool



To Drain or
Reclaim

To Drain or
Reclaim

**Solvent Cleaner -
GenSolve™ 525-D
60C Strip + DI, 45-90sec Cycle**

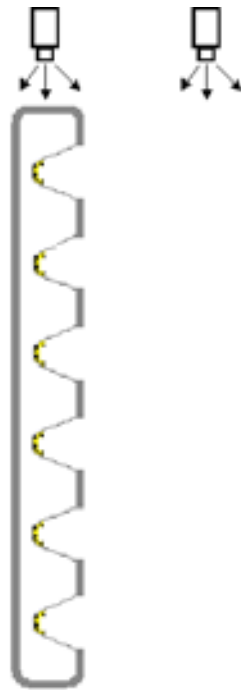
**Throughput
> 150 wph**

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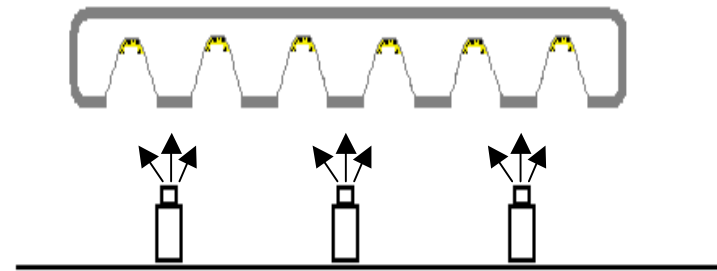
Au Etch Residue Removal

GenSolve™ 525-D (Single Wafer Tool Clean)

Conventional Batch Tool



Single Wafer Spray



**Both tools - Chemical
Recirculation is Recommended**

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Summary

- Described Issues with Etch Process
- Simultaneous Au and Resist Strip
- Front and Backside Via Clean
- Via Cleaning - Quick and Effective
- Agitation Benefits - Batch vs Single Wafer
- Full Process Flow for Chem recirculation

